CR9-98-027B P.M. LiVecchi Performance Enhancements for Threaded Servers 1 of 9

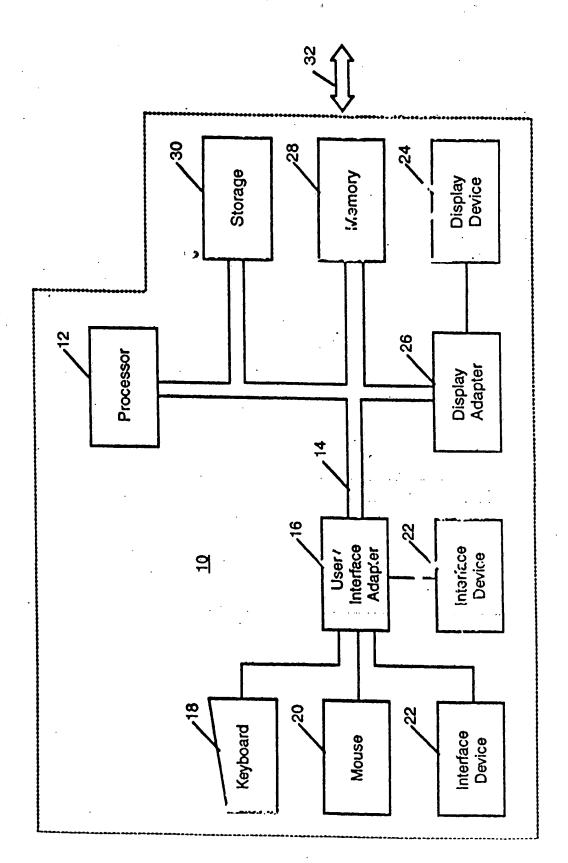
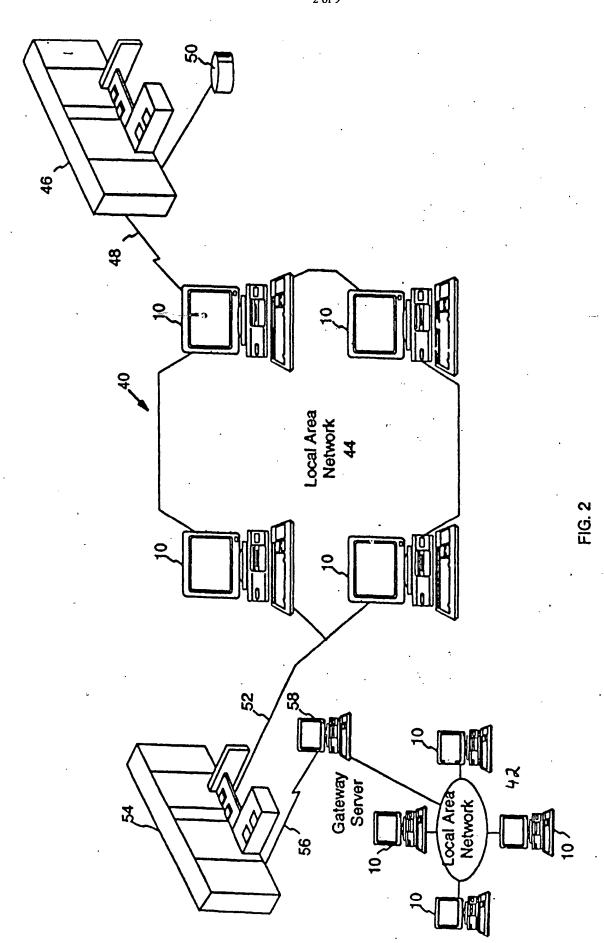


FIG. 1



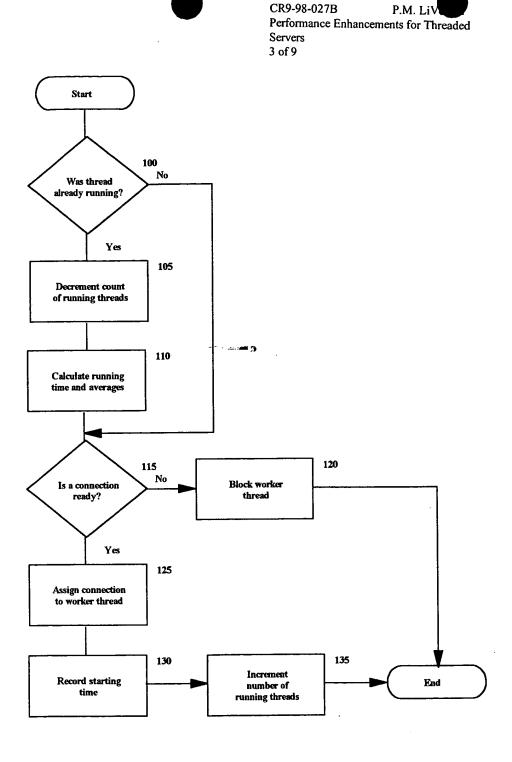


Figure 3A

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Performance Enhance
Servers
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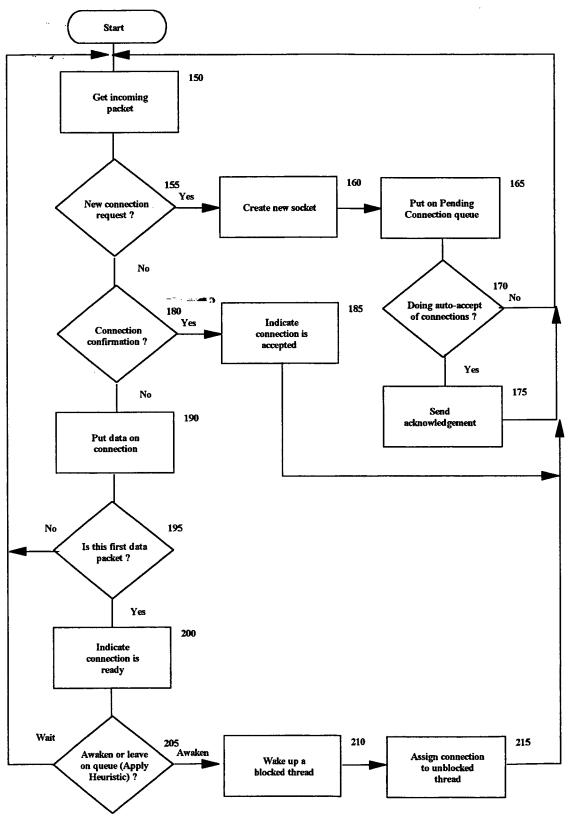


Figure 3B

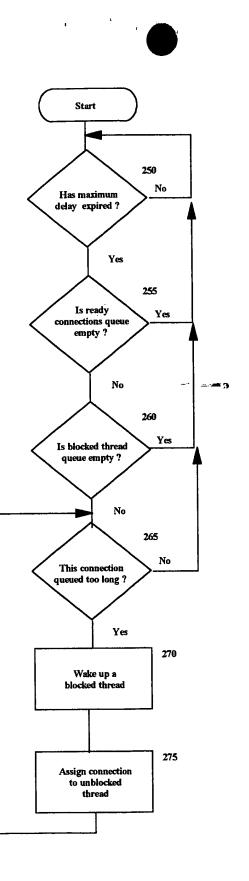


Figure 3C

LiVecchi Threaded

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Servers 5 of 9

Performance Enhanceme

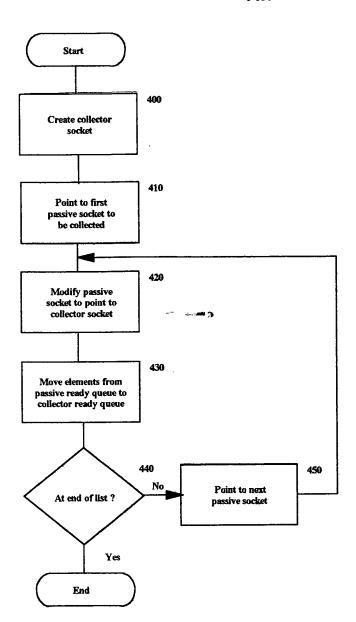


Figure 4

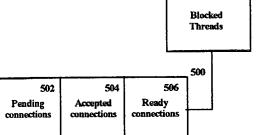


Figure 5A

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Passive Socket

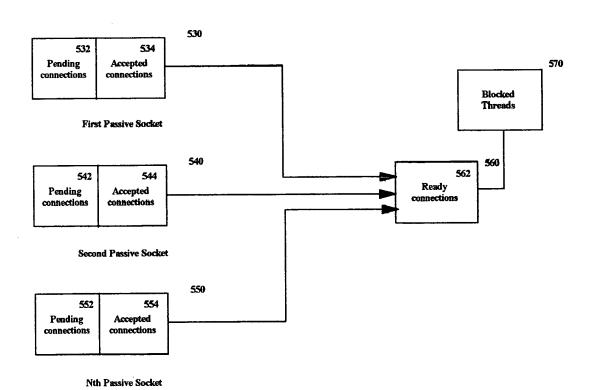
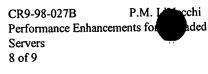


Figure 5B



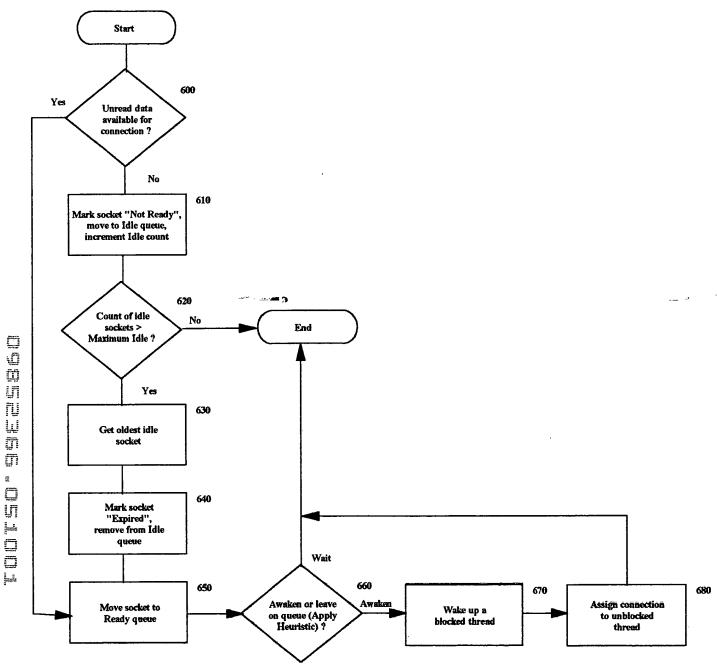
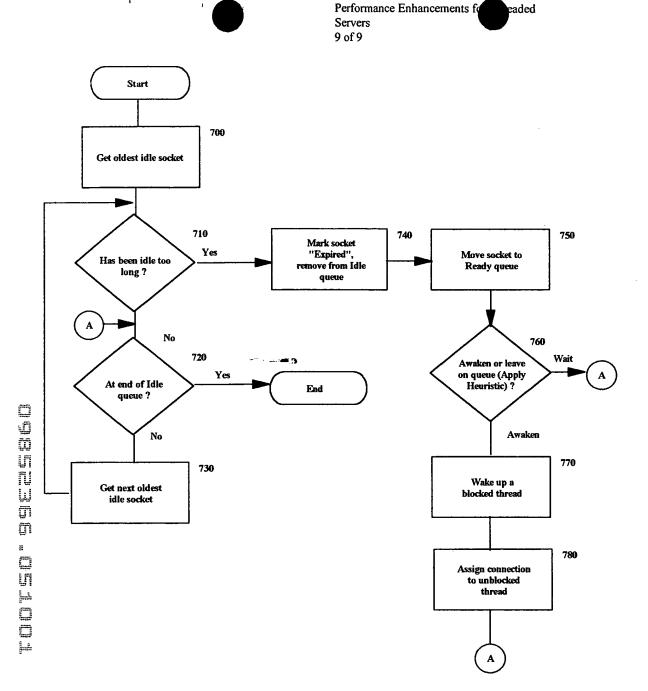


Figure 6A



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Figure 6B